



Eingang der Prioritaetenkette

X14.1	X14.2	X14.3	Signal
X15.1	X15.2	X15.3	

-	-	x	/IODI
-	x	-	/IEI+/IEP
x	-	-	X2:A07 UM

CTC-Beschaltung

X10.1	X10.2	X10.3	Signal
X11.1	X11.2	X11.3	

-	-	x	ZC/T00-CLK/TRG1
-	x	-	ZC/T01-CLK/TRG2
x	-	-	ZC/T02-CLK/TRG3

SpeicherAuswahlsignal

X8.1	X8.2	X8.3	Signal
X9.1	X9.2	X9.3	

x	-	-	MEMDI
-	x	-	MEMDI1
-	-	x	MEMDI2

X6-X7 interner Taktgenerator

X12.1	X12.2	X12.3	X12.4	X12.5	Bemerkung
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-	-	-	-	x	
-	-	-	x	-	
-	-	x	-	-	
-	x	-	-	-	
x	-	-	-	-	